

ARC.003RC1



09/801241

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : David Latta  
Patent No. : 6,988,154 *B2*  
Issued : January 17, 2006  
For : **MEMORY INTERFACE AND  
METHOD OF INTERFACING  
BETWEEN FUNCTIONAL  
ENTITIES**



27299

PATENT, TRADEMARK OFFICE

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March 28, 2006

(Date)

Robert F. Gazdzinski, Reg. No. 39,990

TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attn: Certification of Correction Branch

Certificate  
APR 04 2006  
of Correction

Dear Sir:

Enclosed herewith please find the following documents with regard to the above-identified application:

1. Request For Certificate Of Correction Pursuant To 37 C.F.R. § 1.322 (2 pages);
2. Certificate of Correction (1 page); and
3. Return prepaid postcard.

Respectfully submitted,

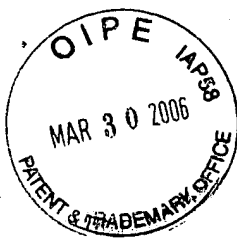
Dated: March 28, 2006

By

Robert F. Gazdzinski  
Registration No. 39,990  
11440 West Bernardo Court, Suite 375  
San Diego, CA 92127  
Telephone: (858) 675-1670  
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APR 04 2006

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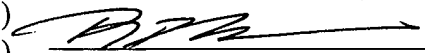
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**REQUEST FOR CERTIFICATE OF CORRECTION**  
**PURSUANT TO 37 C.F.R. § 1.322**

Commissioner for Patents  
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Alexandria, VA 22313-1450  
Attn: Certification of Correction Branch

Dear Sir:

Enclosed for filing is a Certificate of Correction in duplicate in connection with the above-identified patent.

Upon reviewing the above-identified patent, patentee noted a spacing error which should be corrected. The error is a spacing issue contained in Claim 23.

As the error cited in the Certificate of Correction was incurred through the fault of the Patent Office, no fee is believed to be required.

APR 04 2006

**Docket No. : ARC.003RC1**  
**Patent No. : 6,988,154 B2**  
**Issued : January 17, 2006**

The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith to Deposit Account No. 50-1423. A duplicate copy of this paper is enclosed.

Respectfully submitted,

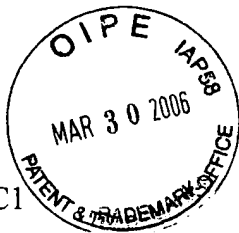
GAZDZINSKI & ASSOCIATES

Dated: March 28, 2006

By: 

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ARC.003RC1



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For : **MEMORY INTERFACE AND  
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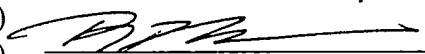
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**COPY**

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,988,154 *B2*  
 DATED : January 17, 2006  
 INVENTOR(S) : David Latta

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 19, Lines 36-39 (Claim 23):

“at least one first means for transferring data and signals to and from a storage device; at least one second means for transferring data and signals to and from a macro function;”

**Should Read:**

--at least one first means for transferring data and signals to and from a storage device;  
 at least one second means for transferring data and signals to and from a macro function;--

MAILING ADDRESS OF SENDER: Robert F. Gazdzinski  
 Gazdzinski & Associates  
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PATENT NO. 6,988,154

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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APR 04 2006

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Gazdzinski & Associates, PATENT NO. 6,988,154  
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